EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Mr. Theodore A. Wood, Reg. No. 52374, on 09/19/2009.

3. The following claims had been amended as:

Claim 2. The system ~~method~~ of claim 18 wherein the ~~a~~ plurality of processors of the homogenous multiprocessor environment are capable of executing a first instruction of a first instruction set and a second instruction of a second instruction set.

Claims 3 and 4, line 1, replace – **method** – with –**system** --.

Claim 5. The system ~~method~~ of claim 3, wherein the computer programs further being configured for ~~comprising~~: converting a functional program of the functional program expressed using first instruction set to an equivalent functional program expressed using the second instruction set.

Claim 6. The system ~~method~~ of claim 3 wherein the queued tasks comprise: x86 processing; graphical image processing; video processing; audio processing; and communication processing.

Claim 7. The system ~~method~~ of claim 3, wherein the computer programs further being configured for ~~comprising~~ : receiving the initial data from a first input/output device.

Claim 8. The system ~~method~~ of claim 3, wherein the computer programs further being configured for ~~comprising~~: passing the resulting data to a first input/output device.

Claim 9, line 1, replace – **method** – with –**system** --.

Claim 10. The system ~~method~~ of claim 9 wherein passing the resulting data through an intermediary device, wherein the intermediary device is coupled to the first input/output device and to a second input/output device further comprising: automatically adapting to a reallocation of the available processing resources among the queued tasks.

Claim 11. The system ~~method~~ of claim 8 wherein passing the resulting data to the ~~a~~ first input/output device further comprises: passing the resulting data to a mixed-signal device.

Claim 12. The system ~~method~~ of claim 3, wherein ~~the step of~~ allocating the available processing resources among the queued tasks is dynamically adjusted.

Claim 16. A method for providing multimedia functionality comprising:

queuing tasks;

~~keep track~~ tracking, remotely from available resources, of ~~the~~ capabilities of ~~all available~~ a plurality of processors in a homogeneous multiprocessor environment in an integrated circuit, wherein each of the ~~available~~ plurality of processors in the integrated circuit is operatively coupled to a bus in the integrated circuit;

identifying, independent of the queued tasks, which of the plurality of processors are available processing resources in the homogenous multiprocessor environment based solely on the ~~capabilities kept track of~~ remote~~ly~~ tracking;

allocating the available processing resources among the queued tasks based on the capability ~~capabilities~~ of ~~each~~ at least one of ~~the available processing resources~~ the available processors in the homogeneous multiprocessor environment to be aggregated with another available processor of the homogenous multiprocessor environment to provide a processing resource, and ~~the~~ processing requirements of each of the queued tasks;

providing to the available processing resources functional programs and initial data corresponding to the queued tasks; and

performing the queued tasks using the available processing resources to produce resulting data, wherein the functional programs cause the available ~~availability~~ processing resources to perform the queued tasks of at least one of: graphic image processing, video processing, audio processing and communication processing.

Claim 17 is canceled.

Claim 18. A system ~~method~~ for providing multimedia functionality comprising:

a plurality of processors in a homogeneous multiprocessor environment in an integrated circuit;

respective memories accessible by each of the plurality of processors; and

computer programs respectively stored in the memories, the computer programs being configured for:

queuing tasks;

identifying, independent of the queued tasks, which of the plurality of processors are available processing resources in the ~~a~~ homogeneous multiprocessor environment ~~in an integrated circuit~~;

allocating the available processing resources among the queued tasks based on the capability ~~capabilities~~ of ~~each~~ at least one of the available processors of the homogeneous multiprocessor environment ~~in the integrated circuit~~ to be aggregated with another available processor of the homogenous multiprocessor environment to provide a processing resource, and the processing requirements of each of the queued tasks, wherein each of the available processors ~~in the integrated circuit~~ is operatively coupled to a bus in the integrated circuit;

providing to the available processing resources functional programs and initial data corresponding to the queued tasks; and

performing the queued tasks using the available processing resources to produce resulting data wherein the functional programs cause the available processing resources to perform the queued tasks of at least one of graphics image processing, video processing, audio processing and communications processing.

Claims 20-23 are canceled.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer N. To whose telephone number is (571) 272-7212. The examiner can normally be reached on M-T 6AM- 3:30 PM, F 6AM- 2:30 PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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